Attorney's Docket No.: 10559-075002 Intel Docket No.: P7567C



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Adiletta et al.

Art Unit : 2183

Serial No.: 10/615,500

Examiner: Ellis, Richard L

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Assignee: Intel Corporation

Title

: PARALLEL PROCESSOR ARCHITECTURE

Mail Stop Amendment **Commissioner for Patents** P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION MAILED 03/17/2005

Claim Listing/Amendments

... page 2

Remarks

... page 4

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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